## CALL FOR PAPERS MIXDES 2017 24 ${ }^{\text {th }}$ International Conference MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS <br>  <br> <br>  

 <br> <br> An invitation to submit paper proposals <br> <br> An invitation to submit paper proposals <br> Bydgoszcz, POLAND, 22 -24 June 2017}Accomodation
The conference will be held in a hotel located in the city centre.
The accommodation will be offered for the Conference participants at conference site.
Intended authors should note the following deadlines:
Receipt of papers March $1^{\text {st }}, 2017$
$\begin{array}{lr}\text { Notification of acceptance of papers } & \text { April 30 } \\ \text { Final paper versions and 1-page abstracts } & \text { May 31 }{ }^{\text {st, }} \mathbf{2 0 1 7}\end{array}$ Price Information
$\begin{array}{ll}\text { Standard registration fee } & 380 \text { Euro } \\ \text { IEEE Members registration fee } & 330 \text { Euro }\end{array}$
 $\begin{array}{ll}\text { Accompanying person social events fee } & 180 \text { Euro }\end{array}$ Conference fee includes: participation in all sessions, MIXDES 2017
Proceedings CD and other conference materials, coffee and tea during



Enquiries Mariusz Orlikowski (Conference Secretary)
Lodz University of Technology Department of Microelectronics and Computer Science (K-25)
e-mail: mixdes2017@dmcs.p.lodz.pl

## WWW

Information on MIXDES 2017 will be available on the World Wide Web
at the following location:

> http://www.mixdes.org


The Venue agglomeration with more than 470,000 inhabitants, Bydgoszcz is the $8^{\text {th }}$ largest city in Poland. The oldest buildings in the city are grouped
in the Old Town, just to mention: the Church of St Martin and Nicolaus, commonly known as Fara Church (a three-aisle late Gothic church erected between 1466 and 1502), the Church of the Assumption of the Holy Virgin
(built between 1582-1602). The three granaries in Grodzka Street, picturesquely located on the Brda River in the immediate vicinity of the old Market Square, are the official symbol of the city. Among the most (Mill Island), located just a few steps from the old Market Square. It was the years thereafter, and it was here that the famous royal mint operated in the $17^{\text {th }}$ century. The city is mostly associated with water, sports, Art
Nouveau buildings, waterfront, music, and urban greenery. It is worth noting of Kazimierz Wielki University, University of Technology and Life Sciences, of Kazimierz Wielki University, University of Technology and Life Sciences, Filharmonia Pomorska concert hall, the Opera Nova opera house. Bydgoszcz is also an important place for contemporary European culture, http://www.visitbydgoszcz.pl/en/explore/what-to-see


Travel Arrangement Travel Arrangement
Air: Bydgoszcz I.J. Paderewski Airport is located in Bydgoszcz, about
3 km south from the city centre. Lufthansa, MistralAir and Ryanair serve 3 km south from the city centre. Lufthansa, MistralAir and Ryanair serve Frankfurt, Glasgow, London (Stansted) and Rome. Rail: Several railway connections to Bydgoszcz from Warsaw, Poznań,
Wrocław, Gdynia and other main towns and cities in Poland are available.
The fast train trip to Warsaw takes about 3-3.5 hours. Rail: Several railway connections to Bydgoszcz from Warsaw, Poznań,
Wrocław, Gdynia and other main towns and cities in Poland are available.
The fast train trip to Warsaw takes about 3-3.5 hours. Coach: Many coach connections are offered between Bydgoszcz and
numerous cities in Poland. Thecoach trip fromWarsawtakes about 4.5 hours. Road: Bydgoszcz is located close to the main Polish roads no. 10 (Warsaw-Szczecin) and 25 (Wrocław-Koszalin). The main Polish east from Bydgoszcz and easy accessible by car.
Areas of Interest The topics of the Conference include:

1. Design of Integrated Circuits and
Design of Integrated Circuits and Microsystems
Design methodologies. Digital and analog synthesis. Hardware-
software sesign methodologies. Digital and analog synthesis. Hardware-
sodesign. Reconfigurable hardware. Hardware description
languages. Intellectual property-based design. Design reuse. 2. Thermal Issues in Microelectronics
2. Thermal and electro-thermal modelling, simulation methods and
tools. Thermal mapping. Thermal protection circuits. 3. Analysis and Modelling of ICs and Microsystems
Simulation methods and algorithms. Behavioural modelling with
VHDL-AMS and other advanced modelling lavguages. Microsystems modelling. Model reduction. Parameter identification.
3. Microelectronics Technology and Packaging
New microelectronic technologies. Packaging. Sensors and actuators. 5. Testing and Reliability
Design for testability and manufacturability. Measurement instruments 6. Power Electroni
Design, manufacturing, and simulation of power semiconductor
devices. Hybrid andmonolithic Smart Power circuits. Power integration.
4. Signal Processing Digital and analogue filters, telecommunication circuits. Neural
networks. Artificial intelligence. Fuzzy logic. Low voltage and low 8. Embedded Syst
5. Embedded Systems
Design, verification and applications.
6. Medical Applications
7. Medical and biotechnology applications. Biometrics. Thermography
in medicine. Official Language
Official language of the Conference is English, neither translation nor
interpretation will be provided. Call for Papers and Contributions A call is made for papers, contributions and other conference activities on
the topics mentioned above. Full papers and 1-page abstracts should be submitted to the Organizing Open Office Writer, LaTeX, together with a generated PDF file). The required format is available on our Web page. Authors are asked to
indicate the topic into which their papers fall. Papers will be reviewed by at least two referees from the International Programme Committee and
the accepted papers will be printed in the proceedings from the author's
Tutorials and Special Sessions - Call for Proposals Several tutorials/special sessions will be held prior to the conference.
Authors willing to propose a tutorial at MIXDES 2017 are invited to send a Authors willing to propose a tutorial at MIXDES 2017 are invited to send a
proposal to the Organizing Committee. The proposal should consist of
a summary including tutorial title, name and affiliation of the lecturer(s), a summary incluaing tutorial title, name and afiliation of the lecturer(s),
tutorial objectives and audience, topical outline and provisional schedule
of the tutorial.

әэиәләјиоэ SヨaXIW ә૫। The MIXDES Conference series started in Debe near Warsaw in 1994
and has been organised yearly in different Polish cities. This time we would like to continue the tradition of inviting you to the most attractive n short period of time the conference has become an important event in
 ve field of design, modelling, simulation, testing and manufacturing in sensors, actuators and power devices as well as their interdisciplinary
applications.

Bydgoszcz, Poland, 22-24 June 2017 Organized by:

Department of Microelectronics and Computer Science,
Institute of Micro- and Optoelectronics
Warsaw University of Technology, Poland
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Poland Section IEEE - CAS \& ED Chapters

Commission of Electronics and Fotonic
of International Union of Radio Science - URSI

 Universitat Politecnica de Catalunya, Spain LAAS - CNRS, Toulouse, France University of Ghent, Belgium $\frac{\pi}{0}$
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## Organizing Committee




$\begin{array}{ll}\text { M. Zubert } & \text { Gdynia Maritime University, Poland } \\ & \text { Lodz University of Technology, Poland }\end{array}$
(Chairman)
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