









ICSES 2018

International Conference on Signals and Electronic Circuits

Kraków, September 10-12, 2018

Second Call for Papers



ABOUT THE CONFERENCE

On behalf of the Program Committee we have a great pleasure to invite you to Kraków for the 10th International Conference on Signals and Electronic Systems - ICSES 2018. The conference, which has been organized in Poland biannually (alternately with ECTTD) since 2000 is an inheritor of the National Polish Conferences on Circuit Theory and Electronics Circuits (KKTOiUE). Every previous edition brought the international scientific community together and proved to be a great opportunity for reporting, sharing and discussions on new theoretical as well as practical aspects of circuits and electronic systems. Starting from 2008 conference has been technically co-sponsored by IEEE and presented papers are indexed in WoS.

SCOPE OF THE CONFERENCE

Includes, but is not limited to:

Signals: analog and digital signal processing, speech and biomedical signals, multirate signal processing, A/D and D/A conversion

Images: image processing, image formation, modeling and representation, image analysis and pattern recognition, image/video coding and transmission, multimedia

Circuits: general circuit theory, methods of circuit analysis, synthesis, design, and optimization, analog and digital filters, ASICs and VLSI design, nonlinear and chaotic circuits, SC and SI circuits, neural networks, RF and microwave circuits

Systems: analog and digital electronic systems, embedded systems, VLSI and large scale systems, data acquisition systems, FPGA-based systems, multimedia systems; RF systems, communication systems; nonlinear, adaptive, and intelligent systems; neural and fuzzy systems, artificial intelligence and expert systems

Mathematical and Computational Methods:

fast algorithms and transforms, simulation and modeling, testing and fault analysis; sensitivity and symbolic analysis, multiresolution analysis and wavelets, neural and fuzzy computing, computer aided design

Applications: electronic systems, signals and image processing and recognizing in medicine, environment, communications

Education: multimedia in education, software for teaching signals and electronic systems, student labs

CONFIRMED INVITED SPEAKERS

Robert Bogdan Staszewski Time-Domain Paradigm of Designing IoT Radios

Kris Iniewski Emerging Semiconductor Sensor Technology for Spectral Computed Tomography

IMPOTRANT DATES

May 6, 2018 Regular papers submission

June 24, 2018 Notification of acceptance

July 8, 2018 Reduced registration fee payment

July 22, 2018 Camera-ready papers

http://www.icses2018.agh.edu.pl

PAPER SUBMISSION

Please sumbit original research work — not published elsewhere nor submitted to another conference/journal. Each paper will be reviewed by at least two reviewers. All the accepted and presented papers will be included in the proceedings e-book and sent to IEEEXplore. Selected papers, in a revised and expanded form may be recommended for publication in international journals.

Paper submission/format – see the conference website for details

CONFERENCE FEES

Standard: 450 EUR

Reduced: 300 - 400 EUR

Standard fee includes single paper registration, admission to all sessions, coffee breaks, lunches and social events. Various reduced rates apply for students, IEEE and PTETIS members provided they are paid before July 8, 2018. Consult webpage for details

TRAVEL, VISAS – see webpage for details or contact the organizers

HOTEL RESERVATION

Conference fee does not include accomodation costs, and each participant has to book a hotel on her/his own.. Kraków has relatively good hotel network, and a special offer for ICSES2018 guests will be prepared by some hotels located close to the venue (eg. "Polonez ***" hotel inside AGH campus)

CONFERENCE VENUE

AGH Computer Science Center Kraków, Kawiory 21

INQUIRES LOCAL COMMITTEE SECRETARY Piotr Dziurdzia, AGH Kraków icses18@agh.edu.pl

PROGRAM COMMITTEE

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